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# TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))

Docket No. QN1062.US

In Re Application Of:

James A. Kunz

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/664,548	09/19/2003	Not Assigned	22145	3679	4294

Title: Buffer to Buffer credit recovery for in-line fibre credit channel credit extension devices



Address to:

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

#### 37 CFR 1.97(b)

1. Mathematical Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

#### 37 CFR 1.97(c)

- 2. 
  The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:
  - ☐ the statement specified in 37 CFR 1.97(e);

OR

☐ the fee set forth in 37 CFR 1.17(p).

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c)) QN1062.US							
In Re Applicatio	n: James A. Kunz		OCT 2 <sup>6</sup>	2004			
Application No.	Filing Date	Examiner	Wales Brues	stomer No.	Group Art Unit	Confirmation No.	
10/664,548	09/19/2003	Not Assigne		22145	3679	4294	
Title: Buffer to B	uffer credit recovery	for in-line fibre credi	t channel c	redit extension (	devices		
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Tejinder Singh Registration No: 39, Klein, O'Neill & Sin 2 Park Plaza, Suite Irvine, CA 92614 Tel: 949 955 1920 Fax: 949 955 1921	gh, LLP						



#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

James A. Kunz

Title: Buffer to Buffer Credit Recovery for In-Line Fibre Credit Channel Extension Devices

Serial No. 10/664,548 Filed: 09/19/2003

Docket No. QN1062.US

#### INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 and 1.98, enclosed materials are brought to the attention of the Examiner for consideration in connection with the above identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached form 1449 be considered by the Examiner and made of record.

Pursuant to 37 C.F.R. § 1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 11-1159 in order to have this Information Disclosure Statement Considered.

If the Examiner has any questions concerning this IDS, the examiner is requested to contact the undersigned. If it is determined that this IDS has been filed under the wrong rule, the PTO is requested to consider this IDS under the proper rule and charge the appropriate fee to Deposit Account No. 11-1159.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 11-1159 for any additional fees required under 37 C.F.R. § 1.16 or under § 1.17; particularly, extension of time fees.

Respectfully submitted,

Klein, O'Neill & Singh, LLP

Dated:

By Tejinder Singh

2 Park Plaza, Suite 510

Irvine, CA 92614 Tel: 949 955 1920

Fax: 949 955 1920

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	INFORMATION DISCLOSURE	CITATION		APPLICANT(S)				
	(Use several sheets if hecess	ary)		James A. Kunz				
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*EXAMINER INITIAL	DOCUMENT NUMBER	DATE		NAME	CLASS	SUBCLASS	FILING DAT	
	6,697,359	02/24/2004	George		370	357		
	6,597,691	07/22/2003	Anderson	ı et al	370	360		
	6,449,274	09/10/2002	Holden et	tal	370	392		
	6,424,658	07/23/2002	Mathur I	ł.	370	429		
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	US-2001/0038628	11/08/2001	Ofek, et a	ıl.	370	392		
	US-2003/0016683	01/23/2003	George et	tal	H04L	12/28		
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	DOCUMENT NUMBER	DATE		COUNTRY	CLASS	SUBCLASS	TRANS YES	NO
	EP0649098	09/19/1994	EP		G06F	15/16	1	
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	WO-98/36537	08/20/1998	PCT		H04L	12/44	1	
	OTHER DOCUME	NTS (Includin	g Author	, Title, Date, Pertinent I	Pages, Etc	.)		
	CLARK, TOM, "Zoning	for Fibre Chan	nel Fabric	s", Vixel Corporation Pape	er - XP00218	85194., (Aug. 19	99), pp 1-6	5
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	Malavalli, Kumar, et al.,	"Distributed Co	mputing y	with fibre channel fabric", P000340745, (Feb. 24, 199	Proc of the	Computer Soc.	Int'l Conf.	Los
	Alamitos, IEEE Comp So	oc. rress., vol. C	лин. э/, X	ruuu340/45, (Feb. 24, 199)	2), pp 269-21	/4		
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APPLICATION NO.

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ATTY DOCKET NO.  QN1062.US	APPLICATION NO. 10/664,548
James A. Kunz	
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		6,330,236	12/11/2001	Ofek, et al	370	369		
		6,324,181	11/27/2001	Wong et al	370	403		
		6,308,220	10/23/2001	Mathur H.	709	238		
		6,160,813	12/12/2000	Banks et al	370	422		
		6,128,292	10/03/2000	Kim et al	370	356		
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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
		US2004/0028038	02/12/2004	Anderson et al	370	į.		
		US2004/0013125	01/22/2004	Betker et al				
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		OTHER DOCUME		g Author, Title, Date, Pertinent F				
		Martin, Charles R., "Fa	bric Interconnec	tion of fibre channel standard nodes"	, Proceedings	of the SPIE, (	(September	8, 1992),
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		Yoshida, Hu, "LUN Sec (1999), pp 1-7	urity Considerat	ions for Storage Area Networks," Hit	achi Data Sys	tems Paper - 2	XP 0021851	93
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Form PTO-A820 (also form PTO-1449) P09C/REV04

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ATTY DOCKET NO.	APPLICATION NO.
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	6,0	47,323	04/01/2000	Krause, Michael R.	709	227		
	6,02	21,128	02/01/2000	Hosoya et al	370	380		
	6,0	14,383	01/11/2000	McCarty, James F.	370	453		
	5,9	99,528	12/07/1999	Chow Henery et al	370	365		
			U.S. PATEN	IT APPLICATION PUBLICATIONS	•	•		
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DAT	
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		OTHER DOCUME	NTS (Includin	l g Author, Title, Date, Pertinent	⊥ Pages, Etc.)	<u> </u>		
		Claudio DeSanti, "Virtu		ch Support" ; VF Switch Support, T			04. pp 1-15.	
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		Pelissier et al, "Inter-Fal	oric Routing", d	lated July 30, 2004, Inter Fabric Roo	uting (04-520v	0); pp 1-31		
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*EXAMINER	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DAT	
	5,987,028	11/16/1999	Yang et al.	370	380		
	5,818,842	10/06/1998	Burwell et al	370	397		
	5,748,612	05/05/1998	Stoevhase et al.	370	230		
	5,687,172	11/11/1997	Cloonan et al	370	395		
	5,610,745	03/11/1997	Bennett, Dwayne	359	230		
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	DeSanti et al, "Virtual	Fabrics"; Virtua	ll Fabrics, T11/03-352v0, May 2003;	рр 1-4			
	Martin et al , "Virtual	Channel Archite	ecture", Presentation by Brocade to	T11/03-369V0	dated June 2	, 2003	
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James A. Kunz	
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	4,821,034	04/11/1989	Anderson et al	340	825			
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Form PTO-A820 (also form PTO-1449)

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Docket Number (Optional)	Application Number
QN1062.US	10/664548
Applicant(s)	
James A Kunz	
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		4,569,043	02/04/1986	Simmons et al	370	63		
		4,546,468	10/08/1985	Christmas et al	370	54		
		4,425,640	01/10/1984	Philip et al	370	58		
		4,162,375	07/24/1979	Schilichte	179	15		
			U.S. PATEN	T APPLICATION PUBLICATION	√S			
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# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

James A. Kunz

Title: Buffer to Buffer Credit Recovery for In-Line Fibre Credit Channel Extension Devices

Serial No. 10/664,548

Filed: 09/19/2003

Docket No. QN1062.US

Mail Stop Amendment Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sirs,

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified application:

Serial/Patent No.	Filing date	Attorney Docket	<u>Title</u>
10/266,360	10/07/2002	QN1010.US	Method And System For Reducing Congestion In Computer Networks
10/241,153	09/11/2002	QN1016.US	Zone Management In A Multi-Module Fibre Channel Switch
10/263,858	10/2003/2002	QN1017.US	Method And System For Using Distributed Name Servers In Multi-Module Fibre Channel Switching
10/212,425	08/05/2002	QN1020.US	Method And System For Flexible Routing In A Fibre Channel System
10/302,149	11/22/2002	QN1027.US	Method And System For Controlling Packet Flow In Networks
10/894579	07/20/2004	QN1044.US	Method and System for Managing Traffic in Fibre Channel Switches
10/894546	07/20/2004	QN1050.US	Method and System for Routing and Filtering Network Data Packets in Fibre Channel Systems
10/894,827	07/20/2004	QN1058.US	Method and System for Selecting Virtual Lanes in Fibre Channel Switches
10/894597	07/20/2004	QN1059.US	Programmable Pseudo Virtual Lanes for Fibre Channel Systems
10/894595	07/20/2004	QN1061.US	Method and System for Reducing Latency and Congestion in Fibre Channel Switches

10/895175	07/20/2004	QN1063.US	Method and System for Detecting Congestion and Over Subscription in a
10/894492	07/20/2004	QN1065.US	Fibre Channel Network  LUN Based Hard Zoning in Fibre  Channel Switches
10/894587	07/20/2004	QN1066A.US	Multi Speed Cut Through Operation in Fibre Channel Switches
10/894726	07/20/2004	QN1066B.US	Method and System for Improving Bandwidth & Reducing Idles in Fibre Channel Switches
10/894629	07/20/2004	QN1066C.US	Method and System for Routing Fibre Channel Systems
10/894491	07/20/2004	QN1066D.US	Method and System for Keeping a Fibre Channel Arbitrated Loop Open During Frame Gaps
10/894536	07/20/2004	QN1066E.US	Method And System For Congestion Control Based On Optimum Bandwidth Allocation In A Fibre Channel Switch
10/894627	07/20/2004	QN1068.US	Method and System for Programmable Data Dependent Network Routing
10/719,077	11/21/2003	QN1069.US	Method and System for Monitoring Events in Storage Area Networks
10/894,689	07/20/2004	QN1070.US	Method and System for Power Control of Fibre Channel Switches
10/798,527	3/11/2004	QN1071.US	Method and System for Preventing Deadlock in Fibre Channel Fabrics using Frame Priorities
10/798468	3/11/2004	QN1072.US	Method and System for Reducing Deadlock in Fibre Channel Fabrics using Virtual Lanes
10/894586	07/20/2004	QN1073.US	Method and System for Buffer to Buffer Credit recovery in Fibre Channel Systems Using Virtual and/or Pseudo Virtual Lane
10/889,635	07/12/2004	QE1075.US	Method And System For Inexpensive And Non-Disruptive Data Capture In Networks
10/889,337	07/12/2004	QE1076.US	Method And System For Minimizing Disruption In Common-Access Networks
10/889259	07/12/2004	QE1078.US	Method And Apparatus For Detecting And Removing Orphaned Primitives In A Fibre Channel Network
10/889267	07/12/2004	QE1079.US	Method And System For Fibre Channel Arbitrated Loop Acceleration
10/889551	07/12/2004	QE1080.US	Method And Apparatus For Testing Loop Pathway Integrity In A Fibre
10/889256	07/12/2004	QE1081.US	Channel Arbitrated Loop Method And Apparatus For Accelerating

Channel Network	Channel N		O1 137		
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10/889255 07/12/2004 QE1082.US Method and Apparatus for Test Pattern	E1082.US Method a	82.US	Method and	pparatus for Test Pa	ittern
Generation				• •	
10/889,588 07/12/2004 QE1083.US Method And Apparatus For Improving	E1083.US Method A	83.US	Method Ar	Apparatus For Impro	ving
Buffer Utilization In Communication					
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Fibre Channel Ports					J
10/894529 07/20/2004 QN1100.US Integrated Fibre Channel Fabri	N1100.US Integrated	00.US	Integrated	ibre Channel F	abric
Controller			_		
10/894732 07/20/2004 QN1101.US Method And System For Congestion	N1101.US Method	01.US	Method A	System For Conge	stion
Control In A Fibre Channel Switch			Control In	bre Channel Switch	
10/957465 10/01/2004 QN1102.US Method and System for Using Boo	N1102.US Method	02.US	Method ar	System for Using	Boot
Servers in Fibre Channel Network				•	
Routing	Routing		Routing		
10/957465 10/01/2004 QN1116.US Method and System for Transferrin	9	16.US	_	System for Transfe	rring
Data directly between storage devices in				=	_
a Storage Area Networks				•	
10/956501 10/01/2004 QN1129.US High Speed Fibre Channel Switch	-	29.US	_		witch
Elements	<u> </u>				
10/956502 10/01/2004 QN1131.US Method and System for LUX	N1131.US Method	31.US	Method	System for	LUN
Remapping in Fibre Channel Networks			Remapping	•	rks
10/08/2004 QN1132.US Fibre Channel Transparent Switch Fo		32.US			
Mixed Switch Fabrics				_	
10/956718 10/01/2004 QN1136.US Method and System for Using an In-Lin	N1136.US Method at	36.US	Method and	stem for Using an In-	Line
Credit Extender with a Host Bus Adapte					

Dated:

Respectfully Submitted,

Tejinder Singh

Attorney for the Applicants, Klein, O'Neill & Singh, LLP 2 Park Plaza, Suite 510

2 Park Plaza, Suite 510 Irvine, CA 92614

Tel: 949 955 1920 Fax: 949 955 1921 Certificate under 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as priority mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on this 22 day of October, 2004.

Name:

Tejinder Singh Signature: